



Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	39mΩ @ V _{GS} = -4.5V	-2.5A
-16V	52mΩ @ V _{GS} = -2.5V	-2.1A
	65mΩ @ V _{GS} = -1.8V	-1.8A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

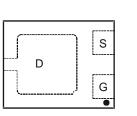
Mechanical Data

- Case: X2-DFN2015-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



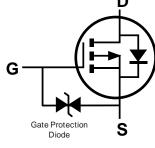
Top View

Bottom View



Internal Schematic

(Top View)



Equivalent Circuit

Ordering Information (Note 5)

Part Number		Case	Packaging		
DMG3415UFY4Q-7		X2-DFN2015-3	3,000/Tape & Reel		
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Greer					

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

•	34P	
	YM	

34P = Marking Code YM = Date Code Marking Y = Year (ex: C = 2015) M = Month (ex: 9 = September)

Date Code Key

Date Obde Rey												
Year	2009	-	~	2015	2016	20)17	2018	2019	20	20	2021
Code	W	-	~	С	D		E	F	G	ŀ	-	I
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	-16	V		
Gate-Source Voltage	V _{GSS}	±8	V		
Continuous Drain Current (Note 7) V _{GS} = -4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	-2.5 -2.2	А
Pulsed Drain Current (Note 7)	I _{DM}	-12	А		

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)		PD	0.65	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ heta}JA$	197	°C/W
Total Power Dissipation (Note 7)		PD	1.35	W
Thermal Resistance, Junction to Ambient (Note 7)	$R_{ hetaJA}$	95	°C/W	
Thermal Resistance, Junction to Case (Note 7)		$R_{\theta JC}$	22	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.) Characteristic **Test Condition** Symbol Min Max Unit Тур OFF CHARACTERISTICS (Note 8) Drain-Source Breakdown Voltage -16 V $V_{GS} = 0V, I_D = -250 \mu A$ BV_{DSS} Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$ -1.0 μA $V_{DS} = -16V, V_{GS} = 0V$ IDSS ____ ±10 μΑ $V_{GS} = \pm 8V, V_{DS} = 0V$ Gate-Source Leakage Igss ____ ±500 'nΑ $V_{GS} = \pm 5V, V_{DS} = 0V$ ON CHARACTERISTICS (Note 8) Gate Threshold Voltage V -0.3 -0.55 -1.0 $V_{DS} = V_{GS}$, $I_D = -250 \mu A$ VGS(TH) 39 31 $V_{GS} = -4.5V, I_D = -4.0A$ 40 52 mΩ V_{GS} = -2.5V, I_D = -3.5A Static Drain-Source On-Resistance RDS(ON) 51 65 V_{GS} = -1.8V, I_D = -2.0A $V_{DS} = -5V, I_D = -2.5A$ Forward Transfer Admittance |Y_{fs}| 7.9 S **DYNAMIC CHARACTERISTICS (Note 9)** Input Capacitance Ciss 282 pF $V_{DS} = -10V, V_{GS} = 0V$ Coss Output Capacitance 152 pF f = 1.0MHzReverse Transfer Capacitance pF 38 Crss ____ 250 Gate Resistance Rg Ω $V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$ Total Gate Charge Qg 10 nC Gate-Source Charge Q_{gs} 1.5 nC $V_{GS} = -4.5V, V_{DS} = -10V, I_D = -4A$ Gate-Drain Charge 2.4 nC Q_{gd} ____ ____ Turn-On Delay Time 79 ns t_{D(ON)} ____ ____ Turn-On Rise Time 175 ns $V_{DS} = -10V, V_{GS} = -4.5V,$ t_R ____ ____ Turn-Off Delay Time 885 $R_{D} = 2.5\Omega, R_{G} = 3.0\Omega$ ____ ____ ns t_{D(OFF)} Turn-Off Fall Time 568 tc ns

Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

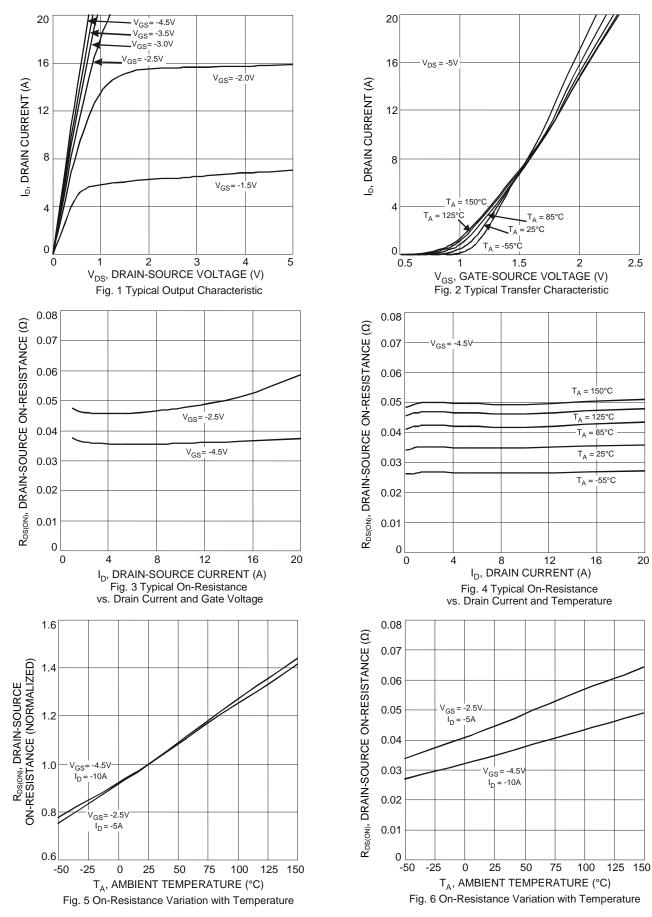
7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.

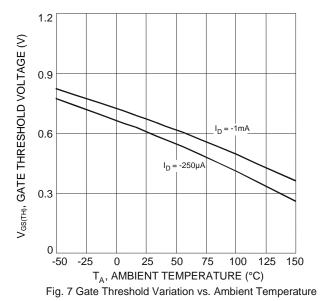


DMG3415UFY4Q





DMG3415UFY4Q



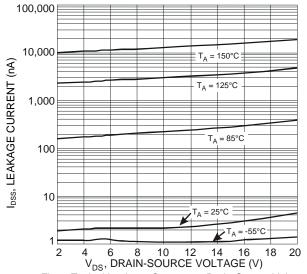
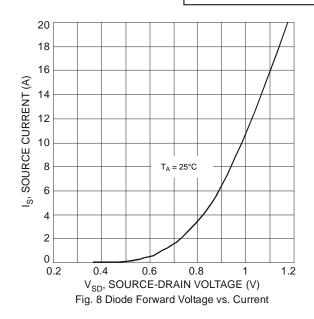
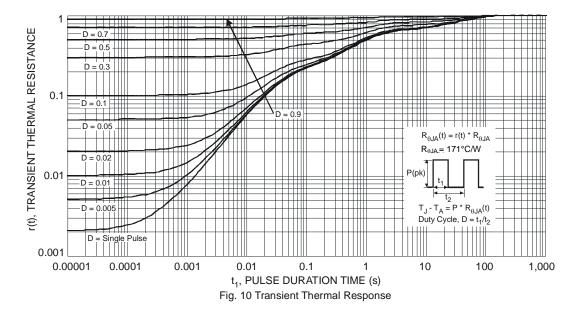


Fig. 9 Typical Leakage Current vs. Drain-Source Voltage



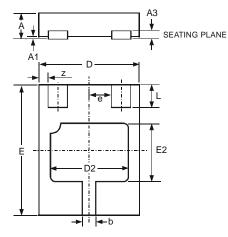




Package Outline Dimensions

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.

X2-DFN2015-3

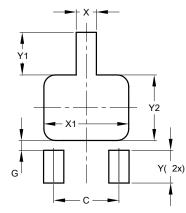


X2-DFN2015-3						
Dim	Min	Max	Тур			
Α	1	0.40	-			
A1	0	0.05	0.02			
A3	-	-	0.13			
b	0.20	0.30	0.25			
D	1.45	1.575	1.5			
D2	1.00	1.20	1.10			
е	-	-	0.50			
Е	1.95	2.075	2.00			
E2	0.70	0.90	0.80			
L	0.25	0.35	0.30			
z	-	-	0.125			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.

X2-DFN2015-3



X2-DFN2015-3				
Dimensions	Value (in mm)			
С	1.000			
G	0.150			
Х	0.310			
X1	1.300			
Ŷ	0.500			
Y1	0.650			
Y2	1.000			



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